

(19) World Intellectual Property Organization  
International Bureau(43) International Publication Date  
19 January 2006 (19.01.2006)

PCT

(10) International Publication Number  
WO 2006/006609 A1(51) International Patent Classification<sup>7</sup>:**H04N 7/26**

(21) International Application Number:

PCT/JP2005/012873

(22) International Filing Date:

6 July 2005 (06.07.2005)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data:

04016437.8 13 July 2004 (13.07.2004) EP

(71) Applicant (for all designated States except US): MAT-SUSHITA ELECTRIC INDUSTRIAL CO., LTD. [JP/JP]; 1006, Oaza Kadoma, Kadoma-shi, Osaka, 5718501 (JP).

(72) Inventors; and

(75) Inventors/Applicants (for US only): WITTMANN, Stefan. WEDI, Thomas. KONDO, Satoshi.

(74) Agent: NII, Hiromori; c/o NII Patent Firm, 3rd Floor, Shin-Osaka Suehiro Center Bldg., 11-26, Nishinakajima 3-chome, Yodogawa-ku, Osaka-shi, Osaka 5320011 (JP).

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SM, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

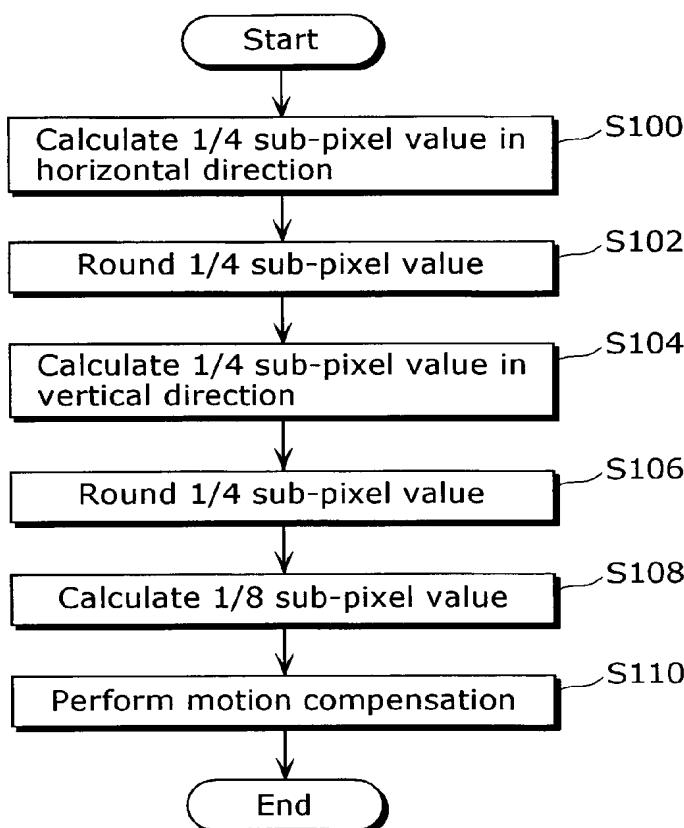
(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, LV, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

**Published:**

— with international search report

[Continued on next page]

(54) Title: MOTION COMPENSATION METHOD



(57) Abstract: A motion compensation method for achieving the reduction of operation workload and the simplification of a hardware configuration includes: interpolation having (i) a first calculation step (S100) of calculating base values of sub-pixel values by multiplying coefficients with pixel values of pixels included in a reference picture and (ii) a first rounding step (S102) of deriving sub-pixel values of sub-pixels by rounding the base values calculated in the first calculation step (S100) in stead of directly using the base values in calculating other sub-pixel values; and motion compensation (S110) of the reference picture that has interpolated sub-pixels with the derived sub-pixel values.

WO 2006/006609 A1



— before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.